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	Application No.	Applicant(s)
Notice of Allowability	10/619.645	AGRAWAL ET AL.
	Examiner	Art Unit
	Jason Crawford	2819
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to 3/20/2006.		
2. The allowed claim(s) is/are <u>1-20</u> .		
3.		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. Interview Summary Paper No./Mail Da 7. Examiner's Amenda	te ment/Comment ent of Reasons for Allowance

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DETAILED ACTION

Allowable Subject Matter

Claims 1-20 are allowed. The following is an examiner's statement of reasons for allowance:

In regards to Claim 1, the prior art does not directly disclose of a programmable logic device comprising input/output buffers adapted to programmably support a plurality of signal types, a serializer deserializer circuit adapted to receive a serial input data stream and extract a clock and provide a parallel data output signal and further adapted to receive a parallel data input signal and provide a serial output data stream and a programmable interconnect adapted to selectively coupled the input/output buffers to the serializer deserializer circuit, nor would it have been obvious to one of ordinary skill in the art to do so. Therefore, Claims 2-9 are also allowed by default as being dependent on the allowable Claim 1.

In regards to Claim 10, the prior art does not directly disclose of a programmable interface circuit within a programmable logic device comprising input/output buffers adapted to support a number of input/output signal types, means for providing serializer/deserializer and clock and data recovery and means for programmably coupling the providing means to input/output buffers for high-speed serial signal streams transferred through the input/output buffers which exceed a core clock rate of the programmable logic device, nor would it have been obvious to one of ordinary skill in the art to do so. Therefore, Claims 11-16 are also allowed by default as being dependent on the allowable Claim 10.

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In regards to Claim 17, the prior art does not directly disclose of a method of providing a programmable interface for a programmable logic device, the method comprising providing buffers adapted to programmable transfer a number of different signal types to and from the programmable logic device and providing a programmable interconnect to selectively couple or decouple an interface circuit having a serializer, deserializer, and clock and data recovery capability, to the buffers depending upon the signal type, nor would it have been obvious to one of ordinary skill in the art to do so. Therefore, Claims 18-20 are also allowed by default as being dependent on the allowable Claim 17.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jason Crawford whose telephone number is 571-272-6004. The examiner can normally be reached on Monday - Friday 7am-4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rex Barnie can be reached on 571-272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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JMC

HEXFORD BARNIE

SUPERVISORY PATENT EXAMINER

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